

## Free Training in MEMS & SiP Technologies

Stimulating education in Micro-Electromechanical Systems (MEMS) and Systems in Package (SiP), promoting microsystems design activities within European universities and research institutes:

- Learn** • by raising skill levels via the provision of training courses.
- Apply** • stimulate to start and increase teaching, research, design and fabrication of MEMS/SiP
- Share** • annual workshop for students and researchers to present and discuss their research.

### Training courses

#### Topics

- Fabrication process
- Design methodologies & design rules
- Analytical modelling for MEMS
- Methods of packaging, systems integration
- Step-by-step instruction in the use of leading CAD tools

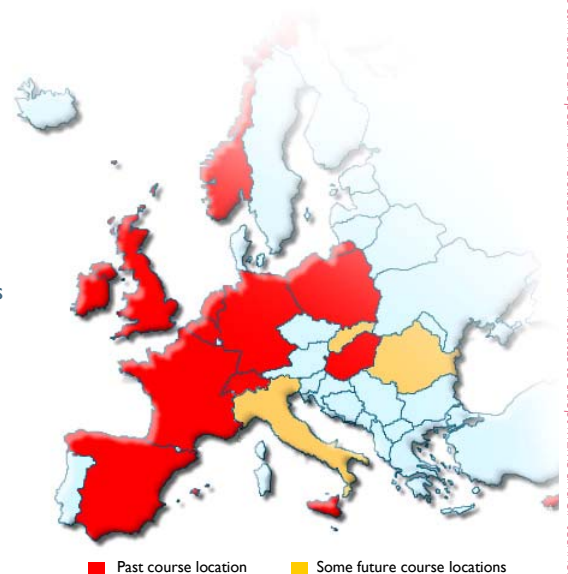


#### Courses (3 to 4 days duration)

- ½ day MEMS introduction
- 1 day on design, modelling, CAD tools, MPW 's
- ½ day introduction to specific MPW Technology :  
(See STIMESI MPW-Technology flyer)
  - SensoNor **MultiMEMS** & Tronics **MEMSOI** processes
  - MEMSCAP : **MUMPS** (Multi-User MEMS) processes
  - QinetiQ : **INTEGRAMplus** Silicon MEMS Prototyping Services
  - IMEC : **RF-SiP** design and RF system integration
- 1 or 2 days 'hands on session' with CAD tools using technology designkit

#### Location

- at different locations in Europe
- open to a pan-European audience
- free to participants from academic and research institutes from the European Union and Norway, Iceland, Lichtenstein, Israel, Croatia, Switzerland and Turkey.



■ Past course location    ■ Some future course locations